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EXPRESS MAIL NO.: EL 500 576 675 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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11-1-02
Mullish

Application No.: 10/042,408

Group Art Unit: 2812

Application of: Fukutomi et al.

Examiner: Nema Berežny

Filed: January 8, 2002

Attorney Docket No.: 7426-0082

For: FABRICATION PROCESS OF
SEMICONDUCTOR PACKAGE AND
SEMICONDUCTOR PACKAGENew York, NY
October 29, 2002**INFORMATION DISCLOSURE STATEMENT**Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to applicant's duty of disclosure under 37 C.F.R. §1.56, enclosed is a PTO Form-1449 listing thirteen (13) references for the examiner's review. Copies of the references are enclosed for the examiner's convenience. These references were cited in two (2) Japanese Office Actions. Copies of the Office Actions and the references are enclosed for the examiner's convenience. Applicants respectfully request that the references be made of record in this application by the examiner's completion and return of the PTO Form-1449.

No fee is believed to be due for this Information Disclosure Statement. Should any fees be required, please charge the same to Deposit Account No. 16-1150.

Respectfully submitted,

Date: October 29, 2002
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Enclosures